ABSTRACT OF THE DISCLOSURE

A system comprises a non volatile memory and a plurality of processors.

The non volatile memory stores an error handling routine. Each processor of the plurality of processors accesses the error handling routine on detecting an error and, on certain errors, signals the remaining processors to enter a rendezvous state. In the rendezvous state, a single processor takes over and performs error handling.

"Express Mail" mailing label number: <u>EV332569896US</u>
Date of Deposit: July 28, 2003
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